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Resume of Chyan Yang, 1988-08

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RESUME OF CHYAN YANG

Chyan Yang was born at Jingchiang, Fukien Province, Republic of China in 1948. He received both his B.S. degree in Electronic Engineering in 1972 and M.S. degree in Management Science in 1977 from National Chiao Tung University (NCTU), Hsinchu, Taiwan, Republic of China. Having taught one year at NCTU on computer programming and optimization in 1978, he went to Georgia Institute of Technology and received his M.S. in Information and Computer Science in 1980. He then worked for Burnham Service Corporation at Columbus, Georgia, a major transportation company specializing in electronic moving and storage. In 1983 he left Burnham and went to the University of Washington, Seattle, Washington. He received his Ph.D. degree in Computer Science in 1987 from the University of Washington.



While at the University of Washington he worked for the department lab as a Graduate Staff Assistant until 1985. He worked for the Northwest Lab for Integrated Systems (LIS) as a Predoctoral Research Assistant from 1985 to 1987. He wrote VLSI generators, designed microprocessors, and tested memory chips at the LIS.

Since 1987 he has been a member of the faculty of the Naval Postgraduate School as an Assistant Professor of Electrical and Computer Engineering. He is responsible for the VLSI Lab setups and course design. His research interests are concerned with the Computer Aided Design for the VLSI chips, the design and analysis of computer architectures, parallel processing, and human brain functions.

Professor Yang's professional activity includes being a Referee for the International Conference on Parallel Processing and IEEE Annual Symposium on Computer Architecture. He is a member of ACM (Association of Computing Machinery) and IEEE (Institute of Electrical and Electronics Engineers).